REMARKS/ARGUMENTS

The Examiner has rejected claims 1 - 11 as anticipated by Farley. Reconsideration of this rejection is respectfully requested. The claims of the present application are drawn to a method and system wherein a processor receives "x inputs" and translates them into a binary system having "y binary outputs", and uses the binary outputs to control the system.

Farley shows in Figure 9 binary numbers which appear to be similar to the binary outputs of the present invention, and translation of these binary numbers to an equivalent base 10 number. What Farley does not show is conversion of some input into the binary numbers shown in Figure 9.

Based upon the foregoing, it is respectfully submitted that Farley does not anticipate the claims of the present invention since it does not disclose or suggest the subject matter related to the "x inputs" which are translated into a binary system.

Based upon the foregoing, it is respectfully submitted that independent claims 1 and 6, as well as their dependent claims, are patentable over the art of record.

An earnest and thorough effort has been made to place this application in condition for allowance. If, upon consideration of this response, the Examiner feels there are issues which can be resolved by telephone interview, the Examiner is respectfully invited to telephone the undersigned.

It is believed that no additional fee is due in connection with this paper. If, however, any fee is due, please charge same to Deposit Account No. 02-0184.

Respectfully submitted,

By _____

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I, Marian R. Capelli, hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313 on August 31, 2004

Marian R. Capelli